1. AFE circuit

* Please confirm CE0, RE0, SE0, DE0 pins from datasheet, and check if it is appropriate for the project purpose. Check if RE0 can be connected to CE0 pin.
* Please check if SPI, I2C, INT\_AFE, GND, VDD signals are needed to be isolated from the controller. If needed ADUM5020, ADUM3151, ADUM1250 can be used as in the EVAL-CN5065 reference design.
* Check if GPIO\_AD\_30 of NXP is SPI MISO, and GPIO\_AD\_31 is SPI MOSI?

1. Hum/TEMP sensor AVDD is replaced with 3V3( in 1V2, AVDD is connected), it should be connected to DVDD, not AVDD, please confirm it again.
2. Heater

Need to choose a proper Isolated DC/DC IC for OCP, OVP, CC, CV operation

This IC should be controlled and monitor driver and sensors by NXP controller.

Voltage and current should be feed to NXP controller.

1. Power

* VSYS and VHeat is supplied from RJ45 connector? Or what is the voltage range?

1. Crypto

In coral board, a crypt IC (A7101CHUK) U6 is already included.

1. NXP controller

* AFE pins are mapped to the NXP controller.
* “08 B2B” sheet is removed, but only unused pins are moved into “16 Interface” sheet.
* I am not sure if “09 Parallel camera” sheet should be remained. If it is needed, INT pin should be assigned to the NXP controller, the original net is mapped to AFE int pin.
* Not sure is “12 DMIC” sheet should be remained.
* Unused pins are placed on “16 Interface” sheet, you can use any necessary pins in accordance with the connecter. But you must carefully use them, because those are 1.8V IOs.

\*\*\*\* NXP IOs are 1.8V based, so a level translator should be added for AFE and other control pins. BE SURE TO CHECK THIS.